# PROJECT: SRMS (-5 MCTU INSTALLED) ASS'Y HOMENCLATURE: MCTU

SYSTEM: ELECTRICAL SUBSYSTEM
ASS'Y P/N: 51155F160-5

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	 	_		_	_

FMEA FME REF. REV	FAILURE HODE AND CAUSE	FAILURE EFFECT ON END ITEM	HDWR / FUNC. RATIONALE FOR ACCEPTANCE 2/18 CAITICALTIY SCREENS: A-PASS B-PASS C-PASS
2475 0		CPU WILL BE RE-INITIALIZED DUE TO MMI. LOSS OF COMMUNICATION WITH ABE, GPC AND DAE. GPC WILL STOP COMMUNICATIONS AFIER THO GPC CYCLES. AUTOBRAKES. ARM COMES TO REST. LOSS OF COMPUTER SUPPORTED MODES. DAC INITIATES AUTO SAFING. LAMP TEST LOST, LOSS OF LIMPING DURING END EFFECTOR CAPTURE. LOSS OF EE AUTO DRIVE HODE. EE AUTO SEQUENCE IN PROGRESS WILL STOP. EE MANUAL ORIVE HODE IS	
		STELL AVAILABLE.  MORSI CASE  UMEXPECTED MOTION, SIX JOINT RUMAWAY. AUTOBRAKES.  REDUNDANT PATHS REMAINING  1) AUTOBRAKES (FOR SAFING THE SYSTEM).  2) DIRECT DRIVE AND EE MANUAL MODE (FOR CONTINUING OPERATIONS).	WHERE APPLICABLE, DESIGN DRAWINGS AND DOCUMENTATION GIVE CLEAR IDENTIFICATION OF HANDLING PRECAUTIONS FOR ESD SENSITIVE PARTS.  BOARD ASSEMBLY DRAWINGS INCLUDE THE REQUIREMENTS FOR SOLDERING STANDARDS IN ACCORDANCE WITH NHB 5300.4(3) AND JSC 08800.  TRANSFORMERS (AND IMDUCTORS) ARE DESIGNED SPECIFICALLY FOR THE APPLICATION. THE DESIGN CRITERIA, INCLUDING CHOICE OF MATERIALS AND TEST REQUIREMENTS ARE IN ACCORDANCE WITH NIL-T-27. MORST CASE STRESS LEVELS DO NOT EXCEED THOSE ALLOWED BY SPAR-RMS-PA.003.  ALL RESISTORS AND CAPACITORS USED IN THE DESIGN ARE SELECTED FROM ESTABLISHED RELIABILITY (ER) TYPES. LIFE EXPECTANCY IS INCREASED BY EMSURING THAT ALL ALLOWABLE STRESS LEVELS ARE DERATED IN ACCORDANCE WITH SPAR-RMS-PA.003. ALL CERAMIC AND ELECTROLYTIC CAPACITORS ARE ROUTTMELY SUBJECTED TO RADIOGRAPHIC INSPECTION.  DISCRETE SEMICONDUCTOR DEVICES SPECIFIED TO AT LEAST THE IN LEVEL OF MIL-S-19500. ALL DEVICES ARE SUBJECTED TO RE-SCREENING BY AN INDEPENDANT TEST HOUSE. SAMPLES OF ALL PROCURED LOIS/DATE CODES ARE SUBJECTED TO DESTRUCTIVE PHYSICAL ANALYSIS (OPA) TO VERIFY THE INTEGRITY OF THE MANUFACTURING PROCESSES. DEVICE STRESS LEVELS ARE, DERATED IN ACCORDANCE

CRITICAL ITEMS LIST

SUPERCEDING DATE: NONE

PROJECT:	SRMS	(-5	MCIU	INSTAL	LED)
ACCIA NUM	FORT	110E -	1170		

SYSTEM:	ELECTRICAL	SUBSYSTEM		
ASS'Y P	/A: <u>51155F18</u>	50-5	SHEET:	_

2475 0	POWER CONDITIONER DIT-1 SCHERATICS B12798 B15444 2559054	MODE: LOSS OF +12V RAIL.  CAUSE(S): (1) O/C OUTPUT 1 NOUCTOR.	CPU WILL BE RE-INITIALIZED DUE 10 MMI. LOSS OF COMMUNICATION WITH ABE, GPC AND DBC. GPPC WILL STOP COMMUNICATIONS AFTER IMO GPC CYCLES. AUTOBRAKES. ARM COMES TO REST. LOSS OF COMPUTER SUPPORTED MODES. DBC 1HITIATES AUTO SAFTING. LAMP TEST LOST. LOSS OF LIMPING DURING END EFFECTOR CAPTURE. LOSS OF EE AUTO DRIVE MODE. EE AUTO	THE DESIGN OF THIS CIRCUIT ACCOMODATES ALL WORST CASE COMPONENT AND OPERATING ENVIRONMENTAL SPECIFICATIONS SUCH THAT ITS SPECIFIED PERFORMANCE REQUIREMENTS ARE MET AT ALL TIMES.
			SEQUENCE IN PROGRESS WILL STOP, EE MAHUAL DRIVE MODE IS STILL AVAILABLE.	
			WORST CASE UNEMPECTED MOTIOM. SIX JOINT RUHAWAY. AUTOBRAKES.  REDUNDANT PATHS REMAINING  1) AUTOBRAKES (FOR SAFING THE SYSTEM).  2) DIRECT ORIVE AND EE MANUAL MODE (FOR CONTINUING OPERATIONS).	N.T. 30

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FHEA

REV.

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FMEA

REF.

2475

NAME, QIY, & DRAWING REF.

DESIGNATION

**CONDITIONER** 

**SCHEMATICS** 

**POWER** 

017-1

612798

815444

2559054

**FAILURE MODE** 

AND

CAUSE

LOSS OF +12V

MODE:

RAIL.

CAUSE(S):

INDUCTOR.

(1) O/C

OUTPUT

PROJEC	T: SRMS	(-5)	MCIU I	NSTALLED)
ASS'Y	<b>MOHENCLA!</b>	URE:	MCTU	

FALLURE EFFECT

END ITEM

RE-INITIALIZED

LOSS OF COMMUNICATION

WITH ABE, GPC

AND DEC. GPC

COMMUNICATIONS AFTER TWO GPC CYCLES.

REST. LOSS OF

WILL STOP

AUTOBRAKES. ARM COMES TO

MODES. DAC INITIATES AUTO

SAFING, LAMP TEST LOST,

LOSS OF LIMPING DURING END

COMPUTER SUPPORTED

EFFECTOR CAPTURE, LOSS

OF EE AUIO

ORIVE MODE. EE AUTO SEQUENCE IN

PROGRESS WILL

WORST CASE

UNEXPECTED

MOTION. SIX JOINT RUNAWAY. AUTOGRAKES. REDUNDANT PATHS REMAINING 1) AUTOGRAKES (FOR SAFING THE SYSTEM). 2) DIRECT DRIVE AND EE MANUAL MODE (FOR CONTINUING OPERATIONS).

STOP. EE MANUAL DRIVE MODE IS STILL AVAILABLE.

CPU WILL BE

DUE TO NHI.

SYSTEM: ELECTRICAL SUBSYSTEM ASS'Y P/N: 51155F160-5 SHEE1: \_\_\_\_3 HDWR / FUNC. 2/1R RATIONALE FOR ACCEPTANCE CRITICALITY SCREENS: A-PASS, B-PASS, C-PASS ACCEPTANCE TESTS THE MCTU IS SUBJECTED TO THE FOLLOWING ACCEPTANCE ENVIRONMENTAL TESTING AS AN LRU. O VIBRATION: LEVEL AND DURATION - REFERENCE TABLE 3.2 O THERMAL: +40 DEGREES C TO -16 DEGREES C (2 CYCLES) QUALIFICATION TESTS THE MCIU IS SUBJECTED TO THE FOLLOWING LAW QUALIFICATION ENVIRONMENTS: O VIBRATION: LEVEL AND DURATION - REFERENCE TABLE 3.2 O SHOCK: BY SIMILARLY TO -3 MCTU O THERMAL: +51 DEGREES C TO -27 DEGREES C (10 CYCLES) G HUMIDITY: BY SIMILARITY TO -3 MC1U MIL-STD-461 AS MODIFIED BY SL-E-0002 (TESIS O EMC: CEO1, CEO3, CSO1, CSO2, CSO6, REO2 (N/B), RSO1, O LIFE: 630 OPERATING HOURS 1000 POWER ON/OFF CYCLES FLIGHT CHECKOUT PDRS OPS CHECKLIST (ALL VEHICLES) JSC 16987

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DATE: 11 JUL 91 CIT REV: 0

PREPARED BY: MING SUPERCEDING DATE: NONE

DESIGNATION  CAUSE  END TIEM  CRITICALITY  CRETICALITY  CRETICALITY  CREENS: A-PASS, 8-  POMER CONDITIONER QTY-1  SCHEMATICS  B12790  B15444  2559054  CAUSE(\$):  CAUSE(\$):  CAUSE(\$):  COMPUNICATION  MITH ABE, GPC AND TEST  APPOCUREMENT, PLANNING, RECEIVING, PRI AND DEC. GPC MILL STOP COMPUNICATIONS  AFTER TWO GPC CYCLES.  ARM COMES TO REST. LOSS OF COMPUNICATIONS  AFTER TWO GPC CYCLES.  ARM COMES TO REST. LOSS OF COMPUNICATIONS  AFTER TWO GPC CYCLES.  ARM COMES TO REST. LOSS OF COMPUNICATIONS  AFTER TWO GPC CYCLES.  ARM COMES TO REST. LOSS OF COMPUNICATIONS  AFTER TWO GPC CYCLES.  ARM COMES TO REST. LOSS OF COMPUNICATIONS  AFTER TWO GPC CYCLES.  ARM COMES TO REST. LOSS OF COMPUNICATIONS  AFTER TWO GPC CYCLES.  ARM COMES TO REST. LOSS OF COMPUNICATIONS  AT VARIOUS LEVELS OF ASSEMBLY AND TEST OF REQUIREMENTS OF SPAR-RNS-PA.003, BY THE SECONDLY REQUIREMENTS. BY AN INDEPENDENT SPAR REQUIREMENTS. BY AN INDEPENDENT SPAR REQUIREMENTS. BY AN INDEPENDENT SPAR RECEIVING INSPECTION VERTIFIES THAT AN DURING END  EFFECTOR CAPTURE. LOSS OF LIMPING DURING END  DAMAGE HAS OCCURRED TO PARTS DURING S DOCUMENT ON THE PROCURRED TO PARTS DURING S DAMAGE HAS OCCURRED TO PARTS	CISED THROUGHOUT DESIGN OCESSING FABRICATION, E MCIU. GOVERNMENT SOURCE ELS OF COMPONENT ASSEMBLY CITION POINTS ARE EMPLOYED ST.
CONDITIONER Q17-1 SCHEMATICS B12798 B15444 C1) O/C OUTPUT INDUCTOR.  CYCLES. AUTOBRAKES. AUTOBRAKEMI DOCUMEN AUTOBRATIONS AND COLUMEN INSPECTION SERIER AND COLUME	OCESSING FABRICATION, E MCIU. GOVERNMENT SOURCE ELS OF COMPONENT ASSEMBLY CITION POINTS ARE EMPLOYED ST.
EE AUTO SCHENCE IN PROGRESS WILL STOP. EE HAMMAL DRIVE WODE IS STILL AVAILABLE.  WORST CASE UMEXPECTED HOTION. SIK JOINT RUMAMAY. AUTOBRAKES.  REDUNDANT PATHS REMAINING 1) AUTOBRAKES.  FOR SAFING THE SYSTEM).  2) DIRECT DRIVE AND EE HAMMAL MODE (FOR CONTINUING OPERATIONS).  EE AUTO SCHEMING DATA CLEARLY TOENTIFIES PARTS ARE INSPECTED INCOUGHOUT MANUFA APPROPRIATE TO THE MANUFACTURING STAC PRINTED CIRCUIT BOARD INSPECTION FOR COR AMD ADEQUACY OF PLATED THROUGH HOLES, IRANIED CIRCUIT BOARD INSPECTION FOR COR PERFORMED USING ULTRAVIOLET LIGHT TEC. DATE OF PERFORMED USING ULTRAVIOLET LIGHT TEC. PART OF PERFORMENT REP.  AND SCREMING TO ME MANUFACTURING STAC AMD ADEQUACY OF PLATED THROUGH HOLES, AMD ADEQUACY OF PLATED THROUGH HOLES, INSTALLATION STATUS AND HARDLARE TO PREAD THROUGH HOLES, AMD ADEQUACY OF PLATED THROUGH HOLES, INSTALLATION STATUS AND HARDLARE THROUGH HOLES, AMD ADEQUACY OF PLATED THROUGH HOLES, INSTALLATION STATUS AND HARDLARE THROUGH HOLES, AMD ADEQUACY OF PLATED THROUGH HOLES, INSTALLATION STATUS AND HARDLARE THROUGH HOLES, INSTALLATION, ALIGNMENT OF BOARDS AMD ADEQUACY OF PLATED THROUGH HOLES, AMD ADEQUACY OF PLATED THROUGH HOLES, INSTALLATION STATUS AND HARDLARE THROUGH HOLES, INSTALLATION, ALIGNMENT OF BOARDS AND ADEQUACY OF PLATED THROUGH HOLES, INSTALLATION ALIGNMENT FOR ADEQUACY OF PLATED THROUGH HOLES, INSTALLATION ALIGN	ALIFIED AT THE PART LEVEL E SPECIFICATION. ALL EEE N, AS A MINIMUM, AS SUPPLIER. ADDITIONALLY, CORDANCE WITH APPROVED TESTING ED BY PA.003 ON A RANDOMLY CES, MINIMUM 3 PIECES FOR ECEIVED. TED TO SPAR-RMS-PA.003.  LL PARTS RECEIVED ARE AS NIS, THAY NO PHYSICAL SNIPHENT, THAT THE TRACEABILITY INFORMATION S ACCEPTABLE PARTS.  ACTURE AND ASSEMBLY AS SE COMPLETED. THESE  TRACK SEPARATION, DAMAGE AND INSPECTIONS ARE AND INSPECTION POINT) CK FOR CORRECT BOARD COPER CONNECTOR CONTACT RES EIC.,  ND CLEANLINESS PECTION POINT) INCLUDES AN AUDIT OF BUILT CONFIGURATION ATORY INSPECTION POINT).

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PI As	ROJECT: SRMS (-5 MC SS'Y MOMENCLATURE: N	TU INSTALLED)	SYSTEM: ELECTRICAL SUBSYSTEM ASS'Y P/N: 51155F160-5	SHEET:	
HODE	FAILURE EFFECT	HDWR / FUNC. 2/1R	RATIONALE FOR ACCEPTANCE	::- <b>=</b>	
	END ITEM	CRITICALITY	SCREENS: A-PASS, B-PASS, C-PASS		
-12V	CPU WILL BE RE-INITIALIZED	QUALITY ASS	WRANCE IN CONJUNCTION WITH ENGINEERING		_

	FMEA REF.	FMEA REV.	HAME, GTY, & DRAWING REF. DESIGNATION	FAILURE MODE AND CAUSE	FAILURE EFFECT ON END ITEM	HOWR / FUNC. RATIONALE FOR ACCEPTANCE 2/1R CRITICALITY SCREENS: A-PASS, B-PASS, C-PASS
	2475		POWER CONDITIONER OIY-1 SCHEMATICS B12798 B15444 2559054	HODE: LOSS OF +12V RAIL.  CAUSE(S): (1) 0/C OUTPUT ENDUCTOR.	CPU WILL BE RE-INITIALIZED DIFE TO MMI. LOSS OF COMMUNICATION WITH ABE, GPC AND DBC. GPC WILL STOP COMMUNICATIONS AFTER TWO GPC CYCLES. AUTOBRAKES, ARN CONES TO REST. LOSS OF COMPUTER SUPPORTED MODES. DBC INITIATES AUTO SAFING. LAMP TEST LOST, LOSS OF LIMPING DURING END EFFECTOR CAPTURE. LOSS OF EE AUTO DRIVE MODE. EE AUTO SEQUENCE IN PROGRESS WILL STOP. EE MANHUAL DRIVE MODE IS STILL AVAILABLE.  WORST CASE UMEKPECTED MOTION. STX JOINT RUMANAY. AUTOBRAKES.  REDUMDANT PATHS REMAINING  1) AUTOBRAKES (FOR SAFING THE SYSTEM). 2) DIRECT DRIVE AND EE MANUAL MODE (FOR CONTINUING OPERATIONS).	OUALITY ASSURANCE IN CONJUNCTION WITH ENGINEERING, RELIABILITY, CONFIGURATION CONTROL, SUPPLIER AS APPLICABLE, AND THE GOVERNMENT REPRESENTATIVE, PRIOR TO THE START OF ANY FORMAL TESTING (ACCEPTANCE OR QUALIFICATION).  ACCEPTANCE TESTING (ATP) INCLUDES AMBIENT, VIBRATION, AND THERMAL TESTING (SPAR/GOVERNMENT REP. MANDITORY INSPECTION POINT).
. P	REPARED BY	: <u>M</u> F	WG	SUPERCEDING DATE:	HOHE	DATE: 11 JUL 91 CIL R

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DA1E: 11 JUL 91 CIL REV: 0

		T			T	ASS'T P/M: <u>51155F16U-5</u>	SHEET:
FMEA REF.	FMEA REV.	NAME OTY & DRAWING REF. DESIGNATION	FAILURE MODE AND CAUSE	FAILURE EFFECT ON END 1TEM	HDWR / FUNC. 2/18 CRITICALITY	RATIONALE FOR ACCEPTANCE SCREENS: A-PASS, B-PASS, C-PASS	
2475	0	POWER CONDITIONER OTY-1 SCHEMATICS 812798 815444 2559054	MODE: LOSS OF +12V RAIL.  CAUSE(S): (1) O/C OUTPUT INDUCTOR.	CPU WILL BE RE-INITIALTZED DUE TO NMI. LOSS OF COMMUNICATION WITH ABE, GPC AND DEC. GPC WILL STOP COMMUNICATIONS AFTER TWO GPC CYCLES. AUTOGRAKES. INITIATES AUTOGRAFING. LAMP TEST LOST. LOSS OF LIMPING DURING END EFFECTOR CAPTURE. LOSS OF EE AUTOGRAVES WILL STOP. EE MANUAL DRIVE MODE. EE AUTOGRAVES. WORST CASE UNEMPECTED MOTION. SIN JOINT RUMAMAY. AUTOGRAKES. REDUNDANT PATHS REMAINING  1) AUTOGRAKES (FOR SAFING THE SYSTEM). 2) DIRECT DRIVE AND EE MANUAL MODE (FOR CONTINUING OPERATIONS).	FAILURE HISTO	EN NO FATLURES ASSOCIATED WITH THIS FAILURE RMS PROGRAM.	
PREPARED BY	: <u>M</u>	FMG	SUPERCEDING DATE	: HONE	-	DATE: 11 JUL 91	

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PROJECT: SRMS (-5 MCTU INSTALLED)
ASS'Y NOMENCLATURE: MCTU

SYSTEM: ELECTRICAL SUBSYSTEM
ASS'Y P/N: 51155F180-5 SHEET: 7

S040237A ATTACHMENT

FMEA REF.	FMEA REV.	HAME, GTY, & DRAWING REF. DESIGNATION	FATLURE MODE AND CAUSE	FAILURE EFFECF ON END ITEM	HOUR / LUNC. RATIONALE FOR ACCEPTANCE  2/1R  CRITICALITY SCREENS: A-PASS, B-PASS, C-PASS	
2475	0	POMER COMDITIONER GTY-1 SCHEMATICS 812798 815444 2559054	MODE: LOSS OF +12V RAIL.  CAUSE(S): (1) 0/C OUTPUT INDUCTOR.	CPU WILL BE RE-INITIALIZED DUE TO NNT. LOSS OF COMMUNICATION WITH ABE, GPC AND DBC. GPC WILL STOP COMMUNICATIONS AFTER IWO GPC CYCLES. AUTOBRAKES. ARM COMES TO REST. LOSS OF COMPUTER SUPPORTED MODES. DBC INITIATES AUTO SAFTING. LAMP TEST LOST. LOSS OF LIMPING DURING END EFFECTOR CAPTURE. LOSS OF EE AUTO DRIVE MODE. EE AUTO DRIVE MODE. EE AUTO SEGUENCE IN PROGRESS WILL STOP. EE MANUAL DRIVE MODE IS STILL AVAILABLE.	OPERATIONAL EFFECT  LOSS OF DATA. AUTOBRAKES. LOSS OF COMPUTER SUPPORTED MODES. LOSS OF LIMPING. LOSS OF EE AUTO MODES. DAC DATA WILL BE INVALID. DIRECT DRIVE AND BACKUP AVAILABLE. EE MODE MANUAL AVAILABLE MITHOUT TALKBACKS.  CREW ACTION  SELECT DIRECT DRIVE. USE EE MODE MANUAL. SINGLE/DIRECT DRIVE SWITCH SHOULD BE PULSED TO MAINTAIN PROPER RATES.  CREW TRAINING  CREW IS TRAINED: TO ALWAYS OBSERVE WHETHER THE ARM IS RESPONDING PROPERLY TO COMMANDS. IF IT ISN'T, APPLY BRAKES. TO RECOGNIZE AND RESPOND TO ALL OFF-NOMINAL OPERATIONS OF THE END EFFECTOR.  MISSION CONSTRAIN!  OPERATE UNDER VERNIER RATES MITHIN 10 FT OF STRUCTURE. THE OPERATOR MUST BE ABLE TO DETECT THAT THE ARM/PAYLOAD IS RESPONDING PROPERLY TO COMMANDS VIA MINDOW AND/OR CCIV VIEWS DURING ALL ARM OPERATIONS.	
				MORST CASE UNEXPECTED HOTION. SIX JOINT RUNAWAY. AUTOBRAKES. REDUNDANT PATHS REMAINING		PAGE 419 OF 471
				1) AUTOBRAKES (FOR SAFING THE SYSTEM). 2) DIRECT DRIVE AND EE MANUAL MODE (FOR CONTINUING OPERATIONS).	PHE P	onte 1 / G

PROJECT: SRMS (-5 MCIU INSTALLED) ASS'Y MOMENCIATURE: MCIU	SYSTEM: ELECTRICAL SUBSYSTEM ASS'Y P/N: 51155F180-5	SHEET:	

FMEA REF.	FMEA REV.	NAME GTY & DRAWING REF. DESIGNATION	FA1LURE MODE AND CAUSE	FABLURE EFFECT ON END ITEM	HOUR / FUNC. 2/1r Criticality		A-PASS, B-F	PASS, C-PASS	
2475		POMER CONDITIONER QTY-1 SCHEMATICS B12798 B15444 2559054	MODE: LOSS OF +12V RAIL.  CAUSE(S): (1) O/C OUIPUT INDUCTOR.	CPU WILL BE RE-INITIALIZED DUE TO MMI. LOSS OF COMMUNICATION WITH ABE, GPC AND D&C. GPC WILL STOP COMMUNICATIONS AFTER TWO GPC CYCLES. AUTOBRAKES. ARM COMES TO RES!. LOSS OF COMPUTER SUPPORTED MODES. D&C INITIALES AUTO SAFING. LAMP TEST LOST. LOSS OF LIMPING DURING END EFFECTOR CAPTURE. LOSS OF EE AUTO DRIVE MODE. EE AUTO DRIVE MODE. EE AUTO DRIVE MODE. EE AUTO DRIVE MODE IS STILL AVAILABLE.  WORST CASE UNEXPECTED MOTION. STR JOINT RUNAWAY. AUTOBRAKES.  REDUNDANT PATHS REMAINING 1) AUTOBRAKES (FOR SAFING THE SYSTEM).  2) DIRECT DRIVE AND EE MANUAL MODE (FOR CONTINUING OPERATIONS).	SCREEN FAILURE	5	rithda, 6'?	nea, t Thas	
REPARED BY:	<u>H</u>	'NG	SUPERCEDING DATE		ELEC - 176			E: <u>11 JUL 91</u>	CIL REV:

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PA AS	ROJECT: SRMS (*5 MC SS'Y NOMENCEATURE: <u>R</u> C	U INSTALLED)	SYSTEM: ELECTRICAL SUBSYSTEM ASS'Y P/N: 51155F160-5	SHEEL: 9
00E	FAILURE EFFECT	HDUR / FUNC. 2/18	RATIONALE FOR ACCEPTANCE	
	END ITEM	CRITICALITY	SCREENS: A-PASS, 8-PASS, C-PASS	1.

FME REF		FAILURE MODE AND CAUSE	FAILURE EFFECT ON END TIEM	HDMR / FUNC. RATIONALE FOR ACCEPTANCE 2/1R CRITICALITY SCREENS: A-PASS, B-PASS, C-PASS
PRE PAREC	POMER CONDITIONER OTY 1 SCHEMATICS 812798 815444 2559054	MODE: LOSS OF +12V RAIL.  CAUSE(S): (1) D/C OUTPUT INDUCTOR.	CPU WILL BE RE-INITIALIZED DUE TO MMI. LOSS OF COMMUNICATION WITH ABE, GPC AND DAC. GPC WILL STOP COMMUNICATIONS AFTER IMO GPC CYCLES. AUTOBRAKES. ARM COMES TO REST. LOSS OF COMPUTER SUPPORTED MODES. DAC INITIATES AUTO SAFING. LAMP TEST LOST. LOSS OF LIMPING DURING END EFFECTOR CAPTURE. LOSS OF EE AUTO DRIVE MODE. EE AUTO DRIVE MODE IS STILL AVAILABLE.  WORST CASE UNEXPECTED MOTION. SIX JOINT RUMAMAY. AUTOBRAKES.  REDUNDANT PATHS REMAINING  1) AUTOBRAKES (FOR SAFING THE SYSTEM).  2) DIRECT DRIVE AND EE MANUAL MODE (FOR CONTINUING OPERATIONS).	OMESO OFFLINE  VARY IMPUT VOLTAGE TO MCIU. VERIFY THE REGULATED VOLTAGES AT  OMESO ONLENE INSTALLATION  HONE  OM: DO ONLINE TURNAROUND  MONITOR MCPC BITE. VERIFY ABSENCE OF BITE BITS.
		1	<del></del>	DATE: 11 JUL 91 CIL REV: 0

CIL REV: 0